

012.17011

Patent

**AMENDMENT****In the Claims:**

1. (currently amended) Customer premise equipment for use in a wireless communication system having a base station and customer sites, the customer premise equipment comprising:

an outdoor comprising:

a transmit path having first tuneable sub-components;

a receive path having second tuneable sub-components;

a detector configured to generate a detector signal;

a controller configured to receive said detector signal, to tune said first tuneable sub-components and said second tuneable sub-components in response to received commands, to transmit information representative of said detector signal in response to a command, and

a memory configured to store instructions to control the operation of the controller; and

an indoor unit comprising a modem for modulating/demodulating data transmitted between the base stations and the customer sites, said indoor unit further comprising a processor configured to transmit commands to said micro controller, to detect a failure in the outdoor unit, and if a failure is detected in the outdoor unit then to write new instructions to said memory.

2. (currently amended) The system of Claim 1, wherein the memory is ~~selected from the group consisting of:~~ comprises at least one of a FLASH memory, a

012.17011

Patent

programmable read only memory (PROM), an application specific integrated circuit (ASIC), a programmable array logic (PAL) integrated circuit and and/or a field programmable gate array (FPGA).

3. (currently amended) The system of Claim 1, wherein the memory is comprises a FLASH memory within a ~~micro~~ the controller.
4. (original) The system of Claim 1, wherein the processor is configured to determine the content of the memory prior to storing the new instructions.
5. (currently amended) The system of Claim 4, wherein the memory comprises memory partitions and the processor is configured to determine the content of each one or more of the memory partitions in the memory.
6. (currently amended) The system of Claim 5, wherein processor is configured to compare the content each of the one or more memory partition partitions in the outdoor unit with the content of the new instructions.
7. (currently amended) The system of Claim 6, wherein the processor is configured to ~~only storing~~ selectively store one or more portions of the instructions that differ from the content of each the one or more memory partition partitions to the outdoor unit.

012.17011

Patent

8. (currently amended) A system for storing updated software instructions to an outdoor unit in a wireless communication system, wherein the wireless communication system comprises a plurality of base stations and customer sites, comprising:

an outdoor unit comprising software instructions stored in a memory, the memory comprising memory partitions;

an indoor unit comprising a copy of the stored software instructions; and

first instructions within the indoor unit for detecting a failure in the outdoor unit, determining content of one or more of the memory partitions in the memory and, responsive to said failure, storing new software instructions to the memory in said outdoor unit.

9. (currently amended) The system of Claim 8, wherein the memory is ~~selected from the group consisting of a:~~ comprises at least one of a FLASH memory, a programmable read only memory (PROM), an application specific integrated circuit (ASIC), a programmable array logic (PAL) integrated circuit and and/or a field programmable gate array (FPGA).

10. (currently amended) The system of Claim 8, wherein the memory is comprises a FLASH memory within a micro controller.

11. (currently amended) The system of Claim 8, wherein the first instructions comprise instructions for determining the content of the one or more memory partitions prior to storing the new software instructions.

012.17011

Patent

12. (cancelled).

13. (currently amended) The system of Claim 12 8, wherein the first instructions comprise instructions for comparing the content of each the one or more memory partition partitions in the outdoor unit with the content of the new software instructions.

14. (currently amended) The system of Claim 13, wherein the first instructions comprise instructions for only selectively storing portions of the software instructions that differ from the content of each one or more memory partition partitions to the outdoor unit.

15. (original) A method for updating software instructions in an outdoor unit of a wireless communications system, wherein the wireless communication system comprises a plurality of base stations and customer sites, comprising:

detecting a failure of the outdoor unit to properly initialize;

reading software instructions from one or more memory partitions in the outdoor unit;

comparing the read software instructions with a stored copy the instructions;

identifying any altered memory partitions that comprise instructions differing from the stored copy; and

storing new software instructions to the altered memory partitions.

012.17011

Patent

16. (currently amended) The method of Claim 15, wherein the memory partitions ~~are selected from the group consisting of:~~ comprising at least one of a FLASH memory partition, a programmable read only memory (PROM) partition, an application specific integrated circuit (ASIC) partition, a programmable array logic (PAL) integrated circuit partition ~~and~~ and/or a field programmable gate array (FPGA) partition.

17. (currently amended) The method of Claim 15, wherein the memory partitions ~~are~~ comprise FLASH memory partitions within a ~~micro~~ controller.

18. (original) The method of Claim 15, wherein detecting a failure comprises instructing the outdoor unit to change operational modes and thereafter determining whether the outdoor unit changed operational modes.

19. (original) The method of Claim 15, wherein reading software instructions comprises transmitting frequency shift key modulated software instructions from the outdoor unit to an indoor unit.

20. (original) The method of Claim 15, comprising counting the number of times that a memory partition is written.

21. (original) The method of Claim 20, comprising determining when a predetermined number of memory partition writes has taken place.

012.17011

## Patent

22. (original) A method for programming an outdoor unit of a wireless communication system, wherein the wireless communication system comprises a plurality of base stations and customer sites, comprising:

instructing the outdoor unit to change to a designated operational mode;

determining whether the outdoor unit changes to the designated operational mode, wherein a determination that the outdoor unit did not change indicates an outdoor unit failure;

reading software instructions from one or more memory partitions in the outdoor unit;

comparing the read software instructions with a stored copy the instructions;

identifying any altered memory partitions that comprise instructions differing from the stored copy; and

storing new software instructions to the altered memory partitions.

23. (currently amended) The method of Claim 22, wherein the memory partitions are ~~selected from the group consisting of:~~ comprises at least one of a FLASH memory partition, a programmable read only memory (PROM) partition, an application specific integrated circuit (ASIC) partition and and/or a gate array (FPGA) partition.

24. (currently amended) The method of Claim 22, wherein the memory partitions are comprise a FLASH memory partitions within a ~~micro~~ controller.

25. (original) The method of Claim 22, wherein instructing the outdoor unit to change operational modes comprises sending a control message to the outdoor unit.

012.17011

## Patent

26. (original) The method of Claim 22, wherein reading software instructions comprises transmitting frequency shift key modulated software instructions from the outdoor unit to an indoor unit.

27. (original) The method of Claim 22, comprising counting the number of times that a memory partition is written.

28. (original) The method of Claim 27, comprising determining when a predetermined number of memory partition writes has taken place.